



Xilinx



Image may be representation. See specifications for product details.

XCZU9CG-L2FFVC900E

Part Number:

XCZU9CG-L2FFVC900E

Manufacturer/Brand:

Xilinx

Product description

IC FPGA 204 I/O 900FCBGA

Datasheets:

 [XCZU9CG-L2FFVC900E.pdf](#)

RoHs Status

 Lead free / RoHS Compliant

Stock Condition

27 pcs stock

Ship From

Hong Kong

Shipment Way

DHL/Fedex/TNT/UPS/EMS

REQUEST FOR QUOTATION

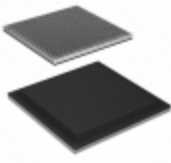
Specifications of XCZU9CG-L2FFVC900E

PART NUMBER	XCZU9CG-L2FFVC900E
MANUFACTURER	Xilinx
DESCRIPTION	IC FPGA 204 I/O 900FCBGA
LEAD FREE STATUS / ROHS STATUS	Lead free / RoHS Compliant
QUANTITY AVAILABLE	27 pcs
DATA SHEET	 XCZU9CG-L2FFVC900E.pdf
SUPPLIER DEVICE PACKAGE	900-FCBGA (31x31)
SPEED	533MHz, 1.3GHz
SERIES	Zynq® UltraScale+™ MPSoC CG
RAM SIZE	256KB
PRIMARY ATTRIBUTES	Zynq®UltraScale+™ FPGA, 599K+ Logic Cells
PERIPHERALS	DMA, WDT
PACKAGING	Tray
PACKAGE / CASE	900-BBGA, FCBGA
OPERATING TEMPERATURE	0°C ~ 100°C (TJ)
NUMBER OF I/O	204
MOISTURE SENSITIVITY LEVEL (MSL)	4 (72 Hours)
LEAD FREE STATUS / ROHS STATUS	Lead free / RoHS Compliant
FLASH SIZE	-
DETAILED DESCRIPTION	Dual ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™ System On Chip (SOC) IC Zynq® UltraScale+™ MPSoC CG Zynq®UltraScale+™ FPGA, 599K+ Logic Cells 256KB 533MHz, 1.3GHz 900-FCBGA (31x31)
CORE PROCESSOR	Dual ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™
CONNECTIVITY	CANbus, EBI/EMI, Ethernet, I²C, MMC/SD/SDIO, SPI, UART/USART, USB OTG
ARCHITECTURE	MCU, FPGA

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