



Xilinx



Image may be representation. See specifications for product details.

XCZU7EG-L1FBVB900I

Part Number:	XCZU7EG-L1FBVB900I
Manufacturer/Brand:	Xilinx
Product description	IC FPGA 204 I/O 900FCBGA PDF 1.XCZU7EG-L1FBVB900I.pdf PDF 2.XCZU7EG-L1FBVB900I.pdf PDF 3.XCZU7EG-L1FBVB900I.pdf PDF 4.XCZU7EG-L1FBVB900I.pdf
Datasheets:	
RoHs Status	RoHS Lead free / RoHS Compliant
Stock Condition	29 pcs stock
Ship From	Hong Kong
Shipment Way	DHL/Fedex/TNT/UPS/EMS

REQUEST FOR QUOTATION

Specifications of XCZU7EG-L1FBVB900I

PART NUMBER	XCZU7EG-L1FBVB900I
MANUFACTURER	Xilinx
DESCRIPTION	IC FPGA 204 I/O 900FCBGA
LEAD FREE STATUS / ROHS STATUS	Lead free / RoHS Compliant
QUANTITY AVAILABLE	29 pcs
DATA SHEET	PDF 1.XCZU7EG-L1FBVB900I.pdf PDF 2.XCZU7EG-L1FBVB900I.pdf PDF 3.XCZU7EG-L1FBVB900I.pdf PDF 4.XCZU7EG-L1FBVB900I.pdf
SUPPLIER DEVICE PACKAGE	900-FCBGA (31x31)
SPEED	500MHz, 600MHz, 1.2GHz
SERIES	Zynq® UltraScale+™ MPSoC EG
RAM SIZE	256KB
PRIMARY ATTRIBUTES	Zynq®UltraScale+™ FPGA, 504K+ Logic Cells
PERIPHERALS	DMA, WDT
PACKAGING	Tray
PACKAGE / CASE	900-BBGA, FCBGA
OPERATING TEMPERATURE	-40°C ~ 100°C (TJ)
NUMBER OF I/O	204
MOISTURE SENSITIVITY LEVEL (MSL)	4 (72 Hours)
LEAD FREE STATUS / ROHS STATUS	Lead free / RoHS Compliant
FLASH SIZE	-
DETAILED DESCRIPTION	Quad ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™, ARM Mali™-400 MP2 System On Chip (SOC) IC Zynq® UltraScale+™ MPSoC EG Zynq®UltraScale+™ FPGA, 504K+ Logic Cells 256KB 500MHz, 600MHz, 1.2GHz 900-FCBGA (31x31)
CORE PROCESSOR	Quad ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™, ARM Mali™-400 MP2
CONNECTIVITY	CANbus, EBI/EMI, Ethernet, I²C, MMC/SD/SDIO, SPI, UART/USART, USB OTG
ARCHITECTURE	MCU, FPGA

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