

PMEG4010CEA

40 V, 1 A low VF MEGA Schottky barrier rectifier
18 November 2016

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a very small SOD323 (SC-76) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 1 A
- Reverse voltage: V_R ≤ 40 V
- Low forward voltage typ. V_F = 720 mV
- Low reverse current typ. I_R = 1.5 μA
- · Very small SMD plastic package
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications
- Automotive applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5 ; f = 20 kHz; $T_{sp} \le 110$ °C; square wave		-	-	1	А
V_R	reverse voltage	T _j = 25 °C		-	-	40	V
V _F	forward voltage	I_F = 1 A; t_p ≤ 300 μs; δ ≤ 0.02 ; T_j = 25 °C		-	720	840	mV
I _R	reverse current	V _R = 40 V; T _j = 25 °C; pulsed	[1]	-	1.5	8	μA
		V _R = 40 V; T _j = 125 °C; pulsed	[1]	-	1	8	mA

[1] Very short test pulse to keep junction temperature unchanged.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	1 2	1 1 2
2	А	anode	SOD323	sym001

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMEG4010CEA	SOD323	plastic surface-mounted package; 2 leads	SOD323			

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4010CEA	4M

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage	T _j = 25 °C		-	40	V
I _F	forward current	$T_{sp} \le 110 ^{\circ}\text{C}; \delta = 1$		-	1	Α
I _{F(AV)}	average forward current	δ = 0.5 ; f = 20 kHz; $T_{sp} \le 110$ °C; square wave		-	1	А
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	2	А
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	8	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	380	mW
			[2]	-	555	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
fro	thermal resistance from junction to ambient	III IIEE ali	[1] [2]	-	-	330	K/W
			[1] [3]	-	-	225	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	45	K/W

^[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

^[4] Soldering point of cathode tab.

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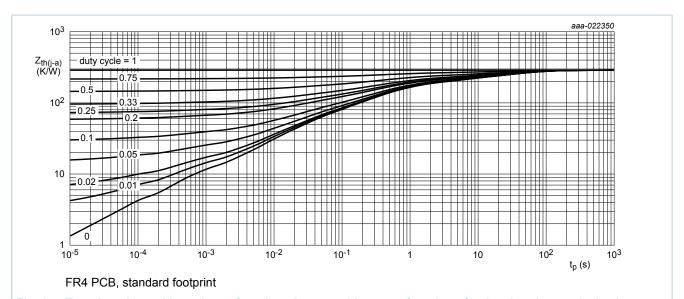


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

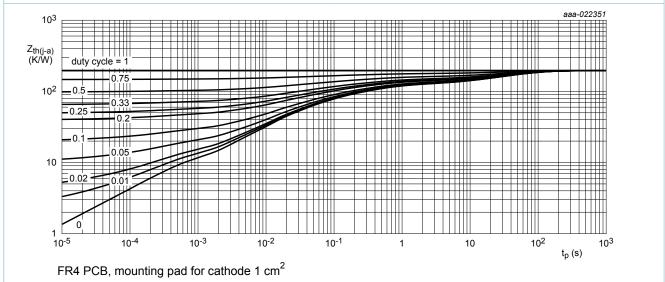


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	$I_R = 1 \text{ mA}; t_p \le 300 \mu\text{s}; \delta \le 0.02 ;$ $T_j = 25 ^{\circ}\text{C}$		40	-	-	V
V _F	forward voltage	$I_F = 10 \text{ mA}; t_p \le 300 \mu\text{s}; \delta \le 0.02 ;$ $T_j = 25 ^{\circ}\text{C}$		-	300	380	mV
		I_F = 100 mA; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_j = 25 °C		-	390	470	mV
		I_F = 200 mA; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_j = 25 °C		-	435	510	mV
		I_F = 500 mA; $t_p \le 300 \ \mu s; \ \delta \le 0.02$; T_j = 25 °C		-	550	640	mV
		I_F = 1 A; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_j = 25 °C		-	720	840	mV
		I_F = 1 A; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_j = 125 °C		-	650	-	mV
R	reverse current	$V_R = 30 \text{ V}; T_j = 25 ^{\circ}\text{C}; \text{ pulsed}$	[1]	-	1	5	μΑ
		$V_R = 40 \text{ V}; T_j = 25 ^{\circ}\text{C}; \text{ pulsed}$	[1]	-	1.5	8	μΑ
		V _R = 40 V; T _j = 125 °C; pulsed	[1]	-	1	8	mA
S_d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	24	-	pF
		V _R = 4 V; f = 1 MHz; T _j = 25 °C		-	13.5	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	9	-	pF
rr	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_i = 25 ^{\circ}\text{C}$		-	1.8	-	ns

^[1] Very short test pulse to keep junction temperature unchanged.

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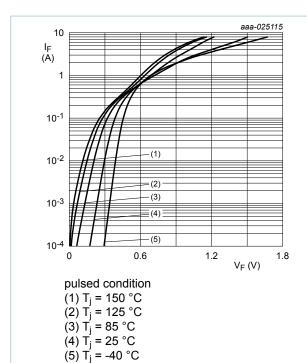


Fig. 3. Forward current as a function of forward voltage; typical values

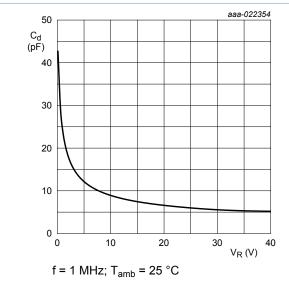


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

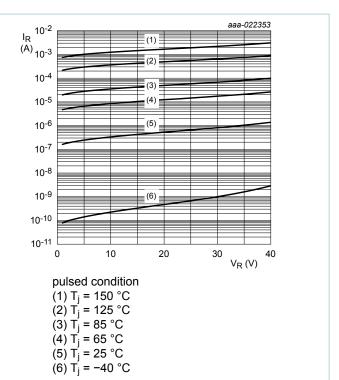
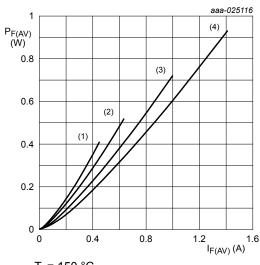


Fig. 4. Reverse current as a function of reverse voltage; typical values



 $T_j = 150 \,^{\circ}\text{C}$ $(1) \,^{\circ} = 0.1$ $(2) \,^{\circ} = 0.2$ $(3) \,^{\circ} = 0.5$ $(4) \,^{\circ} = 1 \,^{\circ} (DC)$

ig. 6. Average forward power dissipation as a function of average forward current; typical values

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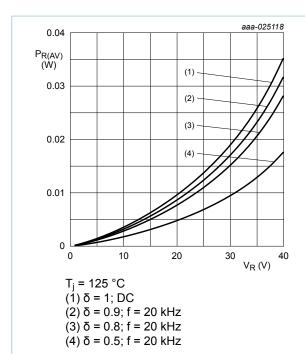


Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values

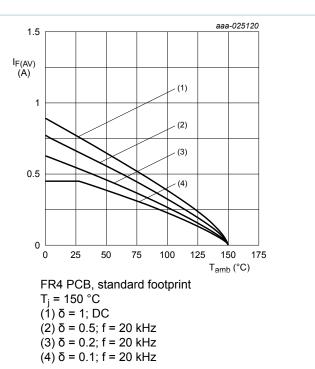


Fig. 8. Average forward current as a function of ambient temperature; typical values

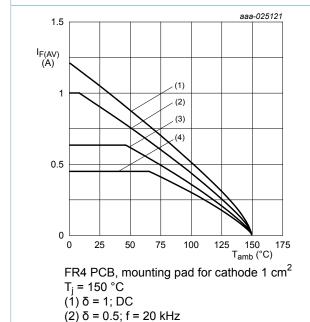
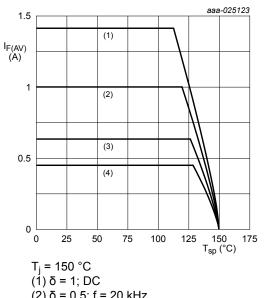


Fig. 9. Average forward current as a function of ambient temperature; typical values

(3) δ = 0.2; f = 20 kHz

(4) δ = 0.1; f = 20 kHz



 $(1) \delta = 1; DC$ $(2) \delta = 0.5; f = 20 \text{ kHz}$ $(3) \delta = 0.2; f = 20 \text{ kHz}$ $(4) \delta = 0.1; f = 20 \text{ kHz}$

Fig. 10. Average forward current as a function of solder point temperature; typical values

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11. Test information

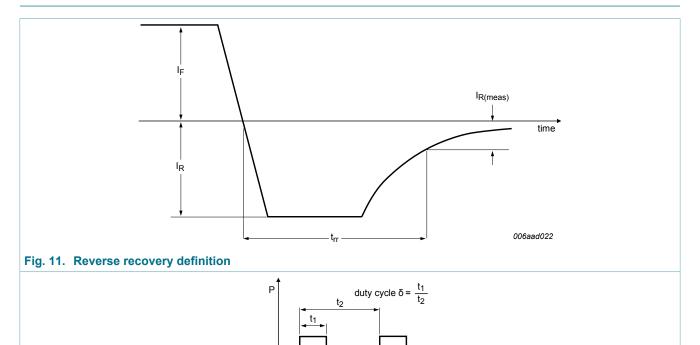


Fig. 12. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_{M} \times \delta$ with I_{M} defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_{M} \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

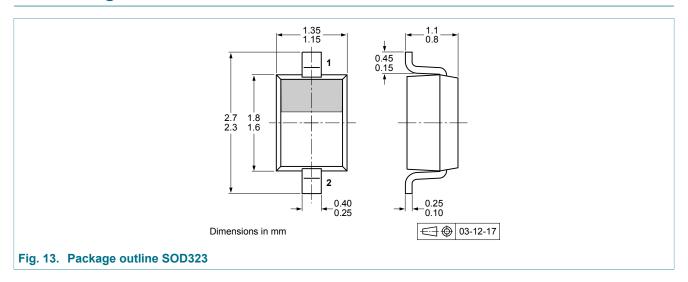
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Quality information

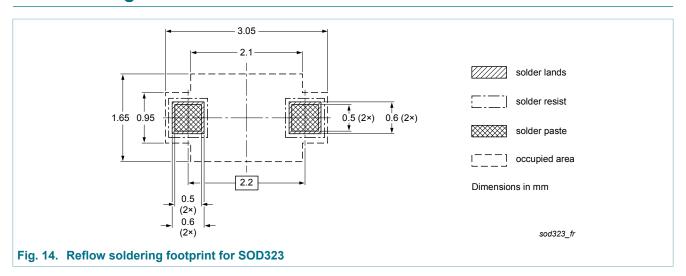
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline

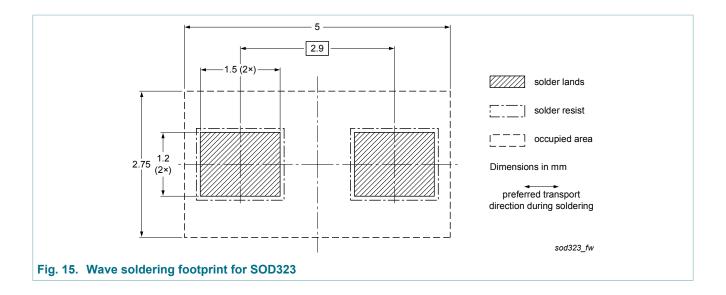


13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4010CEA v. 1	20161118	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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