# Panasonic

# NEW

Programmable Controller



CE

Select the functions you need and control various devices!





Advanced features &

ost saving

Total of 512 inputs and outputs possible



Maximum of 16 units (64 units with bus expansion)

#### Total 256 inputs

- DC input: Max. 256 points
   High-speed counter: Max. 64 channels
   Interrupt input: Max. 128 channels
- Total 256 outputs
- Transistor output: Max. 256 points
- Pulse output: Max. 64 channels
- PWM output: Max. 64 channels
- · Comparison output: Max. 128 channels

# Control FPWIN GR7

# Easily accomplish complex control! Rich in support functions for programs that utilize many functions.

Initial settings screen (Function allocation setting) Easily select the functions to use and the I/O number allocations.

## Function settings Detailed settings 0410 2170 2171 2172 2173 2174 2175 2175 2176 2177 Terminal layout OK Canoel

Configurator PMX (Setting tool for positioning output) To set the positioning table, simply select the parameters at the configuration screen.

Table number	Operation p	Control method	X axis (CH0) M_	Acceleration.	Acceleration _	Deceleration _	Target spe	
1	E: End point	E Increment	0	L: Linear	100	100	1000	
2	E: End point	1 Increment	0	Li Linear	100	100	1000	
3	E End point	1 Increment	0	Litinear	100	100	1010	
4	E End point	1 Increment	0	L Linear	100	100	1010	F
5	E End point	1 Increment	0	L Linear	100	100	1010	
8	E End point	1 Increment	0	L: Linear	100	160	1030	
7	E End point	1 Increment	0	L' Linear	100	100	1000	1
8	E End point	1 Increment	0	L' Linear	180	100	1080	
9	E: End point	1 Increment	0	L: Linear	100	100	1080	
18	E End point	1 Increment	0	L: Linear	100	100	1010	
11	E: End point	1 Increment	0	L: Linear	188	100	1010	
< < > > < o	HIX 1Axis)							
-								

In the window for unit memory access, simply select from the list and click either the "Read" or the "Write" button to build the transfer commands on the ladder. You no longer need to consult the manual nor worry about incorrect data sizes.

PB:		Select Unit: Skit 1: Multi I/O Unit					
.1	9 [7] · [Daplay comments [1] #38	select Bunction:	<b>1</b>				
_	Wr.Q. 777 Stubio115 DB: Fread	High-speed counter     Comparison output     Pulse output     Information monitor area					
		Select Unit Hemory:	Template input				
		Application RW - Unit men					
	U	CH0: Elapsed value RW 2 UH00111 CH0: Preset value RW 2 UH00111	Select Unit: Slot 1: Multi I/O	Unit			
		CHO Lover Inst setting R/W 2 LM00126 CHO: Upper Inst setting R/W 2 LM00126 CHO: Input Requency measure, R 2 LM00126 CHO: Elapsed value R/W 2 LM00126	Select Eunction:				
		OHI: Preset value RW 2 UH0013J	High-speed counter				
			Comparison output				
		Counter preset value Preset value at the time of	nulse output				
		Specify within the setting range of upper and lower limits.	Information monitor area				
		goerand Beed B	Select Unit Memory:	RW	-	Unit memory	y
	Unit memor	v list	CH0: Elapsed value	RW	2	UM00110	
		,	CH0: Preset value	RW	2	UM00118	
			CH0: Lower limit setting	RW	2	UM00120	
			CH0: Upper limit setting	RW	2	UM00128	
			CH0: Input frequency measu	R	2	UM00130	
			CH1: Elapsed value	RW	2	UM00112	
	Explanation of unit ma	mori	CH1: Preset value	RW	2	UM0011A	
۰.	Explanation of unit me	mory	<b>I</b>			•	-
			- Description:				
			Counter preset value (Preset	/alue at	the	time of	
			counter reset input) Specify within the setting rang limits.	e of up	per a	ind lower	
			limits.	e or up	per a	ind lower	

## Product types

	Product name	Standard program capacity	Max. program capacity	Operation speed	Ethernet function	SD memory card function	Encryption function (Note 2) (Note 3)	Part No.
		196 k steps	234 k steps	From 11 ns	Built-in	Built-in	-	AFP7CPS41E
	Standard model	120 k steps	120 k steps	From 11 ns	Built-in	Built-in	-	AFP7CPS31E
		120 k steps	120 k steps	From 11 ns	-	Built-in	-	AFP7CPS31
FP7 CPU units		196 k steps	234 k steps	From 11 ns	Built-in	Built-in	Built-in	AFP7CPS41ES
	Security enhanced type	120 k steps	120 k steps	From 11 ns	Built-in	Built-in	Built-in	AFP7CPS31ES
		120 k steps	120 k steps	From 11 ns	-	Built-in	Built-in	AFP7CPS31S
	Best value model	64 k steps	64 k steps	From 14 ns	-	-	-	AFP7CPS21

Notes: 1) One end unit is attached to the CPU unit. 2) When exporting to China, please use a CPU that does not have an encryption function. 3) For CPU units with encryption function, please use the security enhanced type programming tools.

#### Unit lineup (extract)

Product name Numbe		Number of points	Connection method	Specifications	Part No.
Input unit (DC input) 16		16 points	Terminal block	12 to 24 V DC, common polarity: +/- common, input time constant setting	AFP7X16DW
Output unit [Transistor output, sink (NPN)]		tor 16 points Terminal block Load current: 1.0 A, 5 A/common, 16 points/common		AFP7Y16T	
Multi input/output unit		Input: 16 points	MIL connector	Input: Total 16 points •DC input: Max. 16 points •High-speed counter: Max. 4 channels (1 channel: 4 points) •Interrupt input: Max. 8 points Output: Total 16 points / Torpictor until the Max. 8 points	NEW AFP7MXY32DWD
	Positioning type	Output: 16 points	MIL CONNECTOR	Points - Points - Manuels (in channels (in channels - 4 points) - Comparison output: Max. 4 points) - Points -	NEW AFP7MXY32DWDH
High-speed counter units		2 channels	MIL connector	Liner counter / ring counter, Individual input: 1 multiple, 2-multiple, Direction distinction input:	AFP7HSC2T
		4 channels	WIL connector	1 multiple, 2-multiple, 2-phase input: 1 multiple, 2-multiple, 4-multiple	AFP7HSC4T
Dul		2 axes	MIL connector	Transister 1 pps to 500 kpps	AFP7PG02T
Puise output units		4 axes	MIL COINECTOR		AFP7PG04T

Note: Trapezoidal control with acceleration / deceleration not yet supported.

#### Programming tools

Product name			Туре	Specifications	Part No.
Programming software for Windows <sup>®</sup> Control FPWIN	Japan	lese version	Supports only CPU without encryption function	Windows®10 (32 bit / 64 bit) /	AFPSGR7JP
		Security enhanced type	Supports both CPU with / without encryption function	Windows®8 (32 bit / 64 bit) / Windows®8.1 (32 bit / 64 bit) /	AFPSGR7JPS
	Englis	h version	Supports only CPU without encryption function	Windows®7 SP1 and over	AFPSGR7EN
GR7		Security enhanced type	Supports both CPU with / without encryption function	Vista SP2 / XP SP3	AFPSGR7ENS
Programming software for Windows <sup>®</sup> Control FPWIN Pro7	Englis Chine	sh, Japanese, Korean and se	Supports only CPU without encryption function	Windows®10 (32 bit / 64 bit) / Windows®8 (32 bit / 64 bit) / Windows®8.1 (32 bit / 64 bit) /	AFPSPR7A
		Security enhanced type	Supports only CPU with encryption function * The encryption function will be offered in the future.	Windows®7 SP1 and over (32 bit / 64 bit) / Vista SP2 / XP SP3	AFPSPR7AS

Notes: 1) Windows<sup>®</sup> 10, 8, 7, Vista and XP are trademarks or registered trademarks of Microsoft Corporation in the United States and other countries. 2) When exporting to China, CPU without encryption function is required.

#### Specifications

#### **Control specifications (AFP7CPS21)**

	Item	AFP7	CPS21			
	Memory selection pattern (Note 1)	1 (Factory default)	2			
Memory	Program (steps) (Note 2)	64,000	32,000			
capacity	Data register (words) (Note 2)	131,072	262,144			
	Number of max. program block (PB)	128	64			
Progra	amming method	Relay symbol method				
Contro	ol method	Cyclic operation method				
Progra	am memory	Built-in flash ROM (no back	(up battery required)			
Opera	tion speed	Basic instruction: Min. 14 n	s/step			
Extern	al input (X) / output (Y)	8,192 points (Note 4) / 8,192 po	pints (Note 4)			
Interna	al relays (R)	32,768 points				
Syster	n relays (SR)	Indicate operation status of	f various relays is shown.			
Link re	elays (L)	16,384 points				
Timers (T)		4,096 points: Timer capable of counting (units: 10 μs, 1 ms, 10 ms, 100 ms or 1 sec.) × 4,294,967,295				
Count	ers (C)	1,024 points, Counter capable	of counting 1 to 4,294,967,295			
Link d	ata registers (LD)	16,384 words				
Syster	n data registers (SD)	Internal operation status of	various registers is shown.			
Index	registers (I0 to IE)	15 long words / With switching function				
Master	control relay (MCR)	Unlimited				
Numb	er of labels (LOOP)	Max. 65,535 points for each program block (PB)				
Differe	ential points	Unlimited				
Number of step ladders		Unlimited				
Number of subroutines		Max. 65,535 points for eac	h program block (PB)			
Number of interrupt programs		1 periodical interrupt progra	am			
Constant scan		Available (0 to 125 ms)				
Real ti	me clock (Note 3)	Built in. Date backup with b	attery.			
PLC li	nk function	Max. 16 units, link relays: 1,024 (Data transfer and remote prog (Link area allocation is switchable b	points, link registers: 128 words. ramming are not supported) etween the first and the second half)			

 Notes: 1) The factory default setting is pattern 1.

 2) For data register (DT), data up to 262,144 words can be backed up.

 3) Precision of calendar; At 0 °C 32 °F, less than 95 seconds error per month, At 25 °C 77 °F, less than 15 seconds error per month, At 55 °C 131 °F, less than 130 seconds error per month

 4) Hardware configuration governs the actually usable number of I/O points. When I/O points are not actually usable as internal relays.

#### COM port communication specifications (AFP7CPS21)

Item	Specifications
Interface	RS232C, three-wire system, 1 channel (Note)
Transmission distance	15 m 49.213 ft
Transmission speed	300, 600, 1200, 2400, 4800, 9600, 19200, 38400, 57600, 115200, 230400 bits/sec.
Communication method / Synchronous method	Half-duplex system / Start-stop synchronization system
	Stop bit: 1 bit / 2 bits
	Parity: none / odd / even
Transmission format	Data length: 7 bits / 8 bits
	Start code: with STX / without STX
	End code: CR / CR + LF / none / ETX
Data transmission order	Transmit from bit 0 in character units.
Communication mode	General-purpose communication, Computer link and MODBUS-RTU

Note: SD, RD and SG terminals are isolated from internal circuits.

#### Dedicated power supply output port specifications for GT series programmable display (AFP7CPS21)

Output terminal	Connecting programmable display model
5 V	For 5 V DC type <b>GT</b> series Programmable Display

#### Function specifications (AFP7MXY32DWD / AFP7MXY32DWDH) AFP7MXY32DWD AFP7MXY32DWDH Item Number of occupied I/O points Input/Output: 64 points each (4 words) Input/Output: 96 points each (6 words) Basic input and output Number of external I/O points Input: 16 points, Output: 16 points None, 0.5 $\mu$ s, 1 $\mu$ s, 2 $\mu$ s, 4 $\mu$ s, 8 $\mu$ s, 16 $\mu$ s, 32 $\mu$ s, 64 $\mu$ s, 96 $\mu$ s, 128 $\mu$ s, 256 $\mu$ s, 2 ms, 4 ms, 8 ms Input time constant setting Setting possible in 2-point units No output, N channel, P channel, Both channels (push pull output), Differential output Output polarity setting Setting possible in 4-point units 8 points/unit Number of points Interrupt (Max. of 8 units can be used with FP7 system.) Non-interrupt unit, Interrupt unit (Set using DIP switches) Mode Interrupt condition setting Terminal input, Comparison match Ring counter Counter type Linear counter Direction distinction, Individual input, Phase input Input mode Number of channels 4 channels (Note 1) Signed 32 bit (-2,147,483,648 to +2,174,483,647) Counting range Setting possible of upper and lower limits 5 V input voltage: 500 kHz (Note 2) 12 V input voltage: 500 kHz (350 kHz with phase input) (Note 2) Counter Max. counting speed 24 V input voltage: 250 kHz (180 kHz with phase input) (Note 2) Min. input pulse width 0.5 µs Max. 8 points Comparison output setting Terminal input counter: 4 channels Transfer multiplication function (× 1, × 2, × 4) Elapsed value offset/preset function Elapsed value hold function, setting of upper/lower count limits Others Input pulse frequency measurement Overflow/underflow detection Number of channels 4 channels Direction distinction, Individual output, Phase Output mode output, Comparison match stop 2 terminals/channel (B11 to B18 terminals) Pulse output function Output output terminals PWM output function 1 terminal/channel (B11, B13, B15 and B17 terminals) 1 to 500 kHz (Note 3) (Settable by 1 Hz) Output Pulse output function Pulse 1 to 100 kHz (Note 3) (Settable by 1 Hz) frequence PWM output function

PWM output function 0 to 100 % (Settable by 0.1%) Pulse number measurement function (dedicated pulse counter 4 channels) Other functions Notes: 1) When using elapsed value hold function, number of channels will be limited. 2) With 50 % duty input pulse. 3) When push pull setting or output current is 0.1 A. Varies according to load.

50 % approx. (Fixed)

Pulse output function

Duty ratio

#### Positioning function specifications (AFP7MXY32DWDH)

	Item	AFP7MXY32DWDH			
Nu	mber of axes controlled	Max. 4 axes			
ons	Position setting mode	Increment, Absolute			
icati	Output interface	Transistor open collector output, Push-pull, Line driver (Note 1)			
Common specif	Pulse output method	Pulse + Sign, CW + CCW			
	Max. output frequency	500 kHz			
	Outptu pulse duty ratio	When using table setting mode: 50 % (Fixed)			
	Control unit	Pulse			
	Position setting range	-1,073,741,824 to +1,073,741,823 pulses			
	Speed command range	Pulse: 1 to 500,000 Hz			
	Max. operation speed	500 kHz			
sition control	Acceleration/ deceleration method	Linear acceleration/deceleration			
	Acceleration time	1 to 10,000 ms (Settable by 1 ms)			
	Deceleration time	1 to 10,000 ms (Settable by 1 ms)			
	Number of positioning tables	20 tables for each axis (Up to 2 tables can be executed consecutively.)			
Pc	Control method (Single axis)	PTP control (E point control, C point control), CP control (P point control), Speed control (J point control) (Note 2) (Note 3)			
	Control method (2-axis linear interpolation)	E point, P point, C point controls, Composite speed or Long axis speed setting			
	Dwell time	0 to 32,767 ms (Settable by 1 ms)			
ion	Speed command range	Pulse: 1 to 500,000Hz (Note 3)			
erat	Acceleration/deceleration method	Linear acceleration/deceleration			
90	Acceleration time	1 to 10,000 ms (Settable by 1 ms)			
ğ	Deceleration time	1 to 10,000 ms (Settable by 1 ms)			
Ε	Speed command range	Pulse: 1 to 500,000 Hz			
etu	Acceleration/deceleration method	Linear acceleration/deceleration			
e	Acceleration time	1 to 10,000 ms (Settable by 1 ms)			
E	Deceleration time	1 to 10,000 ms (Settable by 1 ms)			
Ĭ	Return method	DOG methods (3 types), Home position method, Data set method			
io	Deceleration stop	Performs deceleration stop in the deceleration time of a running operation for each axis.			
Inct	Emergency stop	Stops in a deceleration time specified for the emergency stop for each axis.			
p fl	Limit stop	Stops in a deceleration time specified for the limit input for each axis.			
Stc	System stop	Stops all axes immediately.			
Vote	otes: 1) The number of axes is reduced when setting Line driver.				

The J point control is executable only for the two axes of CH0 and CH1.
 When performing the J point control or JOG operation, the speed can be changed after the startup.

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