International

AUIRF3415

Features

- Advanced Planar Technology
- Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Repetitive Avalanche Allowed up to Tjmax

Specifically designed for Automotive applications, this cellular design of HEXFET® Power MOSFETs

utilizes the latest processing techniques to achieve

low on-resistance per silicon area. This benefit combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in Automotive and

- Lead-Free, RoHS Compliant
- Automotive Qualified*

Description



G	D	S
Gate	Drain	Source

Absolute Maximum Ratings

a wide variety of other applications.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V	43	
I _D @ T _C = 100°	C Continuous Drain Current, V _{GS} @ 10V	30	А
I _{DM}	Pulsed Drain Current ①	150	
P _D @T _C = 25°C	Power Dissipation	200	W
	Linear Derating Factor	1.3	W/°C
V _{GS}	Gate-to-Source Voltage	± 20	V
E _{AS}	Single Pulse Avalanche Energy (Thermally Limited)	590	mJ
I _{AR}	Avalanche Current ①	22	А
E _{AR}	Repetitive Avalanche Energy ①	20	mJ
dv/dt	Peak Diode Recovery dv/dt ③	5.0	V/ns
ТJ	Operating Junction and	-55 to + 175	
Т _{STG}	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds (1.6mm from case)	300	
	Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.1N•m)	

	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-Case ©		0.75	
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.50	_	°C/W
$R_{ ext{ heta}JA}$	Junction-to-Ambient		62	

HEXFET[®] is a registered trademark of International Rectifier. *Qualification standards can be found at http://www.irf.com/

HEXFET[®] Power MOSFET

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	150			V	V _{GS} = 0V, I _D = 250µA
$\Delta V_{(BR)DSS} / \Delta T_J$	Breakdown Voltage Temp. Coefficient		0.17		V/°C	Reference to 25° C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance			0.042	Ω	V _{GS} = 10V, I _D = 22A ④
V _{GS(th)}	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
gfs	Forward Transconductance	19			S	$V_{DS} = 50V, I_{D} = 22A$
I _{DSS}	Drain-to-Source Leakage Current			25	μA	$V_{DS} = 150V, V_{GS} = 0V$
				250	İ	$V_{DS} = 120V, V_{GS} = 0V, T_{J} = 150^{\circ}C$
I _{GSS}	Gate-to-Source Forward Leakage			100	nA	V _{GS} = 20V
	Gate-to-Source Reverse Leakage			-100	Î	V _{GS} = -20V
Dynamic E	lectrical Characteristics @ T _J =	= 25°C	(unle	ss oth	nerwis	e specified)
-	Parameter	Min.	Typ.	Max.	Units	· · ·
Qg	Total Gate Charge			200		I _D = 22A
Q _{gs}	Gate-to-Source Charge			17	nC	V _{DS} = 120V
Q _{gd}	Gate-to-Drain ("Miller") Charge			98		V _{GS} = 10V, See Fig. 6 & 13 ④
t _{d(on)}	Turn-On Delay Time		12			V _{DD} = 75V
t _r	Rise Time		55			I _D = 22A
t _{d(off)}	Turn-Off Delay Time		71		ns	$R_{G} = 2.5 \Omega$
t _f	Fall Time		69		ĺ	$R_D = 3.3 \Omega$, See Fig. 10
L _D	Internal Drain Inductance		4.5			Between lead,
					nH	6mm (0.25in.)
Ls	Internal Source Inductance		7.5		ĺ	from package
						and center of die contact
C _{iss}	Input Capacitance		2400			$V_{GS} = 0V$
C _{oss}	Output Capacitance		640		pF	$V_{DS} = 25V$
C _{rss}	Reverse Transfer Capacitance		340		1	<i>f</i> = 1.0MHz, See Fig. 5
Diode Cha	aracteristics					
	Parameter	Min.	Тур.	Max.	Units	Conditions
I _S	Continuous Source Current			43		MOSFET symbol
	(Body Diode)				А	showing the
I _{SM}	Pulsed Source Current			150		integral reverse
	(Body Diode) ①					p-n junction diode.
V _{SD}	Diode Forward Voltage			1.3	V	$T_J = 25^{\circ}C, I_S = 22A, V_{GS} = 0V$ ④
t _{rr}	Reverse Recovery Time		260	390	ns	T _J = 25°C, I _F = 22A
Q _{rr}	Reverse Recovery Charge		2.2	3.3	nC	di/dt = 100A/µs ④
t _{on}	Forward Turn-On Time	Intrinsio	turn-or	n time is	negliaib	le (turn-on is dominated by LS+LD)

Static Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

Notes:

① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)

v V_{DD} = 25V, starting T_J = 25°C, L = 2.4mH

- $R_G = 25\Omega$, $I_{AS} = 22A$. (See Figure 12)
- 3 I_{SD} \leq 22A, di/dt \leq 820A/µs, V_{DD} \leq V_{(BR)DSS}, T_{\rm J} \leq 175°C
- ④ Pulse width \leq 300µs; duty cycle \leq 2%.
- $\ensuremath{\mathbb{S}}$ $\ensuremath{\mathsf{R}}_{\theta}$ is measured at TJ approximately 90°C.

Qualification Information[†]

Qualification Level		Automotive (per AEC-Q101) ^{††} Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.			
	Machine Model	Class M4 (+/- 800V) ^{†††}			
		AEC-Q101-002			
505	Human Body Model	Class H2 (+/- 3500V) ^{†††}			
ESD		AEC-Q101-001			
	Charged Device	Class C5 (+/- 2000V) ^{†††}			
	Model		AEC-Q101-005		
RoHS Compliant		Yes			

† Qualification standards can be found at International Rectifier's web site: http://www.irf.com/

†† Exceptions to AEC-Q101 requirements are noted in the qualification report.

††† Highest passing voltage.







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10

V_{DS}, Drain-to-Source Voltage (V)

20us PULSE WIDTH TJ = 175 °C

100

1000

I_D, Drain-to-Source Current (A)

100

10

1

тор

VGS 15V 10V 8.0V 7.0V 5.0V 5.5V 5.5V

BOTTOM 4.5V



Fig 3. Typical Transfer Characteristics



Fig 4. Normalized On-Resistance Vs. Temperature



Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage









Fig 8. Maximum Safe Operating Area

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Fig 10a. Switching Time Test Circuit



Fig 10b. Switching Time Waveforms



Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case



Fig 12a. Unclamped Inductive Test Circuit





Fig 12b. Unclamped Inductive Waveforms



Fig 13a. Basic Gate Charge Waveform





Fig 13b. Gate Charge Test Circuit



Peak Diode Recovery dv/dt Test Circuit



* V_{GS} = 5V for Logic Level Devices

Fig 14. For N-Channel HEXFETS

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TO-220AB Package Outline

Dimensions are shown in millimeters (inches)





NOTES: DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994. DIMENSIONIS ARE SHOWN IN INCHES [MILLIMETERS]. LEAD DIMENSION AND FINISH UNCONTROLLED IN L1. DIMENSION D& E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY. DIMENSION DA & c1 APPLY TO BASE METAL ONLY. CONTROLLING DIMENSION : INCHES. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS E.H.1,02 & E1 DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARTIES ARE ALLOWED. 2 3 4

1.- GATE 2.- DRAIN 3.- SOURCE IGBTs, CoPACK

LEAD ASSIGNMENTS HEXFET

> 1.- GATE 2.- COLLECTOR 3.- EMITTER DIODES 1.- ANODE/OPE 2.- CATHODE 3.- ANODE

	DIMENSIONS					
SYMBOL	MILLIM	ETERS	ERS INCHES			
Γ	MIN.	MAX.	MIN.	MAX.	NOTES	
A	3.56	4.82	.140	.190		
A1	0.51	1.40	.020	.055		
A2	2.04	2.92	.080	.115		
b	0.38	1.01	,015	.040		
b1	0.38	0.96	.015	.038	5	
b2	1,15	1,77	.045	.070		
b3	1.15	1.73	.045	.068		
c	0,36	0.61	,014	.024		
c1	0,36	0.56	.014	.022	5	
D	14.22	16.51	.560	.650	4	
D1	8.38	9.02	.330	.355		
D2	12.19	12.88	.480	.507	7	
E	9.66	10.66	.380	.420	4,7	
E1	8.38	8.89	.330	.350	7	
e	2.54	BSC	.100	1		
e1 -	5.0	38	.200	BSC	-	
H1	5.85	6.55	.230	.270	7,8	
L	12.70	14.73	.500	.580		
L1	-	6.35	-	.250	3	
øP	3,54	4.08	.139	.161		
Q	2,54	3,42	,100	,135		
ø	90'-	-93"	90*	1		

TO-220AB Part Marking Information



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/



Ordering Information

Base part number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRF3415	TO-220	Tube	50	AUIRF3415



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