

MOSFETs Silicon N-Channel MOS (U-MOSVI-H)

TK40P04M1

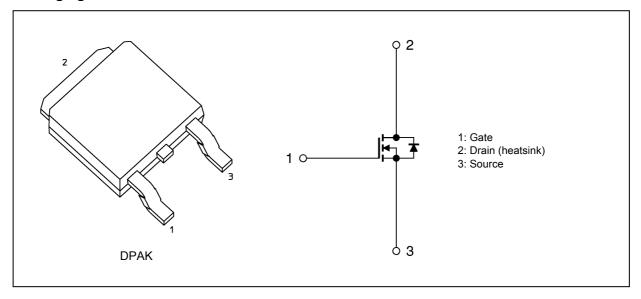
1. Applications

- Switching Voltage Regulators
- · Motor Drivers
- · Power Management Switches

2. Features

- (1) High-speed switching
- (2) Low gate charge: $Q_{SW} = 7.4 \text{ nC (typ.)}$
- (3) Low drain-source on-resistance: $R_{DS(ON)} = 8.5 \text{ m}\Omega$ (typ.) ($V_{GS} = 10 \text{ V}$)
- (4) Low leakage current: I_{DSS} = 10 μA (max) (V_{DS} = 40 V)
- (5) Enhancement mode: V_{th} = 1.3 to 2.3 V (V_{DS} = 10 V, I_{D} = 0.2 mA)

3. Packaging and Internal Circuit





4. Absolute Maximum Ratings (Note) (Ta = 25°C unless otherwise specified)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	40	V
Gate-source voltage		V_{GSS}	±20	
Drain current (DC)	(Note 1)	I _D	40	Α
Drain current (pulsed)	(Note 1)	I _{DP}	120	
Power dissipation (T _c =	= 25°C)	P _D	47	W
Single-pulse avalanche energy	(Note 2)	E _{AS}	41	mJ
Single-pulse avalanche current		I _{AS}	40	Α
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance		2.65	°C/W
Channel-to-ambient thermal resistance	R _{th(ch-a)}	125	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 32 V, T_{ch} = 25°C (initial), L = 20 μ H, R_G = 25 Ω , I_{AS} = 40 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±0.1	μА
Drain cut-off current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V	_	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	40			V
	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	25	_		
Gate threshold voltage	V_{th}	V _{DS} = 10 V, I _D = 0.2 mA	1.3	_	2.3	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 4.5 V, I _D = 20 A	_	10.3	13.4	mΩ
		V _{GS} = 10 V, I _D = 20 A	_	8.5	11	

6.2. Dynamic Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	1920	_	pF
Reverse transfer capacitance	C _{rss}		_	90	_	
Output capacitance	C _{oss}			310		
Gate resistance	r _g	V _{DS} = 10 V, V _{GS} = 0 V, f = 5 MHz	_	1.6	3.5	Ω
Switching time (rise time)	t _r	See Figure 6.2.1.		20		ns
Switching time (turn-on time)	t _{on}			27		
Switching time (fall time)	t _f		_	18	_	
Switching time (turn-off time)	t _{off}		_	63		

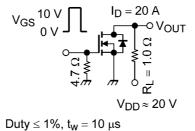


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus	Q_g	$V_{DD} \approx 32 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 40 \text{ A}$	_	29	1	nC
gate-drain)		$V_{DD} \approx 32 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 40 \text{ A}$	_	15		
Gate-source charge 1	Q _{gs1}	$V_{DD} \approx 32 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 40 \text{ A}$	_	6.0		
Gate-drain charge	Q_{gd}		_	4.7		
Gate switch charge	Q_SW		_	7.4		

6.4. Source-Drain Characteristics (T_a = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed)	(Note 3)	I _{DRP}	_	_	_	120	Α
Diode forward voltage		V _{DSF}	I _{DR} = 40 A, V _{GS} = 0 V	_	_	-1.2	V

Note 3: Ensure that the channel temperature does not exceed 150°C.



7. Marking

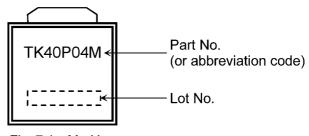
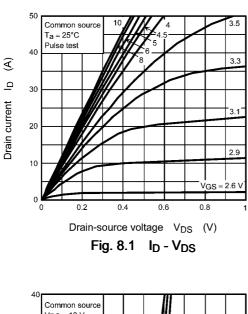
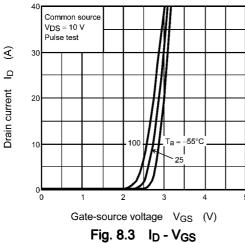
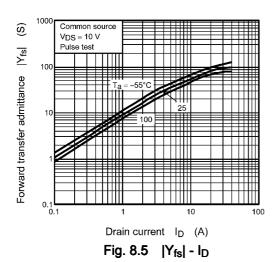


Fig. 7.1 Marking

8. Characteristics Curves (Note)







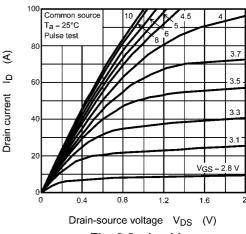


Fig. 8.2 I_D - V_{DS}

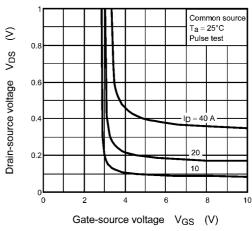


Fig. 8.4 V_{DS} - V_{GS}

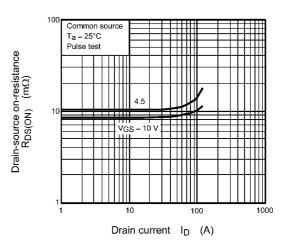


Fig. 8.6 R_{DS(ON)} - I_D

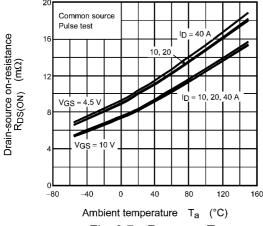


Fig. 8.7 R_{DS(ON)} - T_a

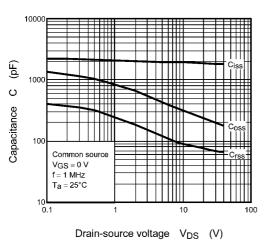


Fig. 8.9 Capacitance - V_{DS}

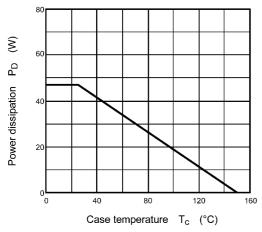


Fig. 8.11 P_D - T_c (Guaranteed Maximum)

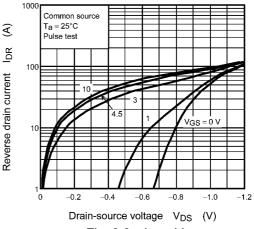


Fig. 8.8 I_{DR} - V_{DS}

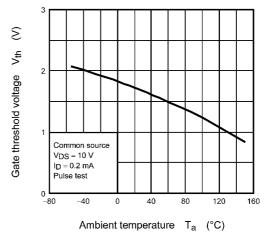


Fig. 8.10 V_{th} - T_a

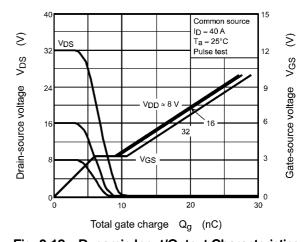


Fig. 8.12 Dynamic Input/Output Characteristics

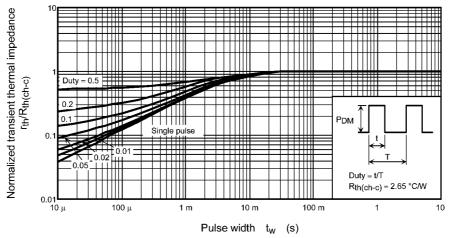


Fig. 8.13 r_{th}/R_{th(ch-c)} - t_w (Guaranteed Maximum)

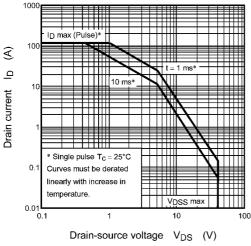


Fig. 8.14 Safe Operating Area (Guaranteed Maximum)

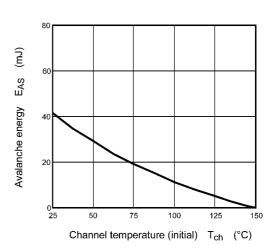


Fig. 8.15 E_{AS} - T_{ch} (Guaranteed Maximum)

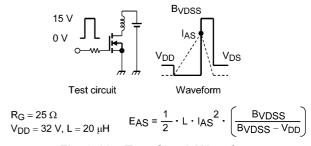


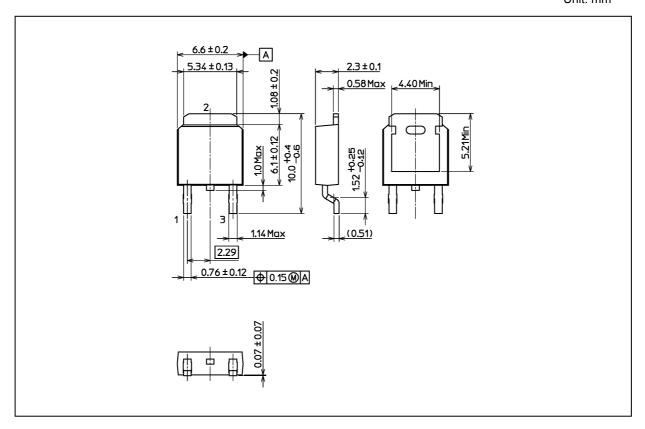
Fig. 8.16 Test Circuit/Waveform

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.36 g (typ.)

Package Name(s)
TOSHIBA: 2-7K1S
Nickname: DPAK



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