TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

# 2SK2993

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain-source ON resistance :  $R_{DS(ON)}$  = 82 m $\Omega$  (typ.)

High forward transfer admittance : |Y<sub>fs</sub>| = 20 S (typ.)
 Low leakage current : I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 250 V)

Enhancement mode : V<sub>th</sub> = 1.5 to 3.5 V (V<sub>DS</sub> = 10 V, I<sub>D</sub> = 1 mA)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	250	$(\sqrt{y})$	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	250	V	
Gate-source voltage			$V_{GSS}$	±20	A
Drain current	DC	(Note 1)	ΙD	20	A
	Pulse	e (Note 1)	I <sub>DP</sub>	60	^
Drain power dissipation (Tc = 25°C)			$P_{D}$	100	W
Single pulse avalanche energy (Note 2)			EAS	423	mJ
Avalanche current			IAR (	20	A
Repetitive avalanche energy (Note 3)			EAR	10	mJ
Channel temperature			((T <sub>ch</sub>	150	/\°C
Storage temperature range			T <sub>stg</sub>	-55 to 150	~C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions" Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	1.25	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

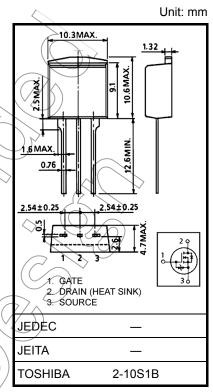
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 1.79 mH,  $I_{AR}$  = 20 A,  $R_G$  = 25  $\Omega$ 

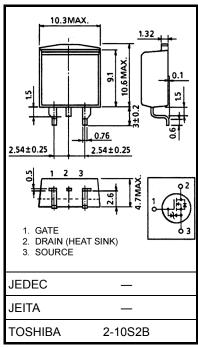
Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device.

Please handle with caution.



Weight: 1.5 g (typ.)



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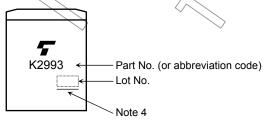
#### **Electrical Characteristics (Ta = 25°C)**

Characteristics Symbol		Symbol	Test Condition		Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μA
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	250	_		V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	1	3.5	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	(F	82	105	mΩ
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 A	70	20	-	S
Input capacitano	ce	C <sub>iss</sub>		$\rightarrow$	4000	1	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		300		pF
Output capacitance		Coss		_	1000	1	
T Switching time F	Rise time	t <sub>r</sub>	$V_{GS} = 10V$ $V_{GS} = 10V$ $V_{DD} = 130V$ $V_{DD} = 130V$	_	15	//	- ns
	Turn-on time	t <sub>on</sub>		-(	35	> _	
	Fall time	t <sub>f</sub>			30	_	
	Turn-off time	t <sub>off</sub>			180		
Total gate charg plus gate-drain		Qg		) _	100	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 200 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		70	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	30	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit		
Continuous drain reverse current (Note 1)	IDR	< (V/s)-	_	_	20	Α		
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	60	Α		
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V	ı	_	-2.0	V		
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V		300	_	ns		
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 A / μs	1	3.3	_	μC		

### Marking

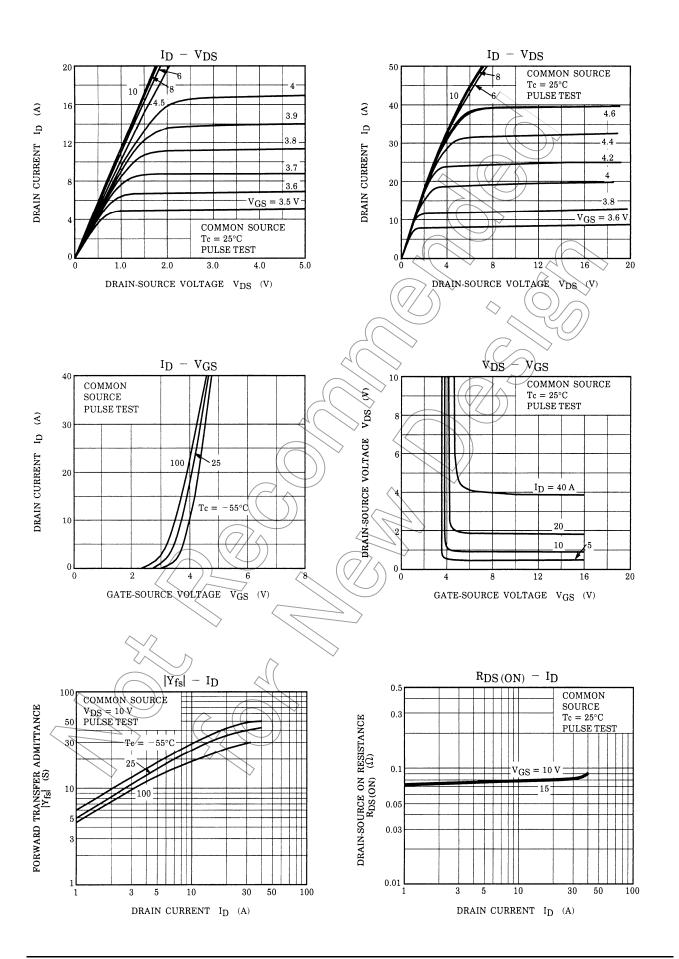


Note 4: A line under a Lot No. identifies the indication of product Labels.

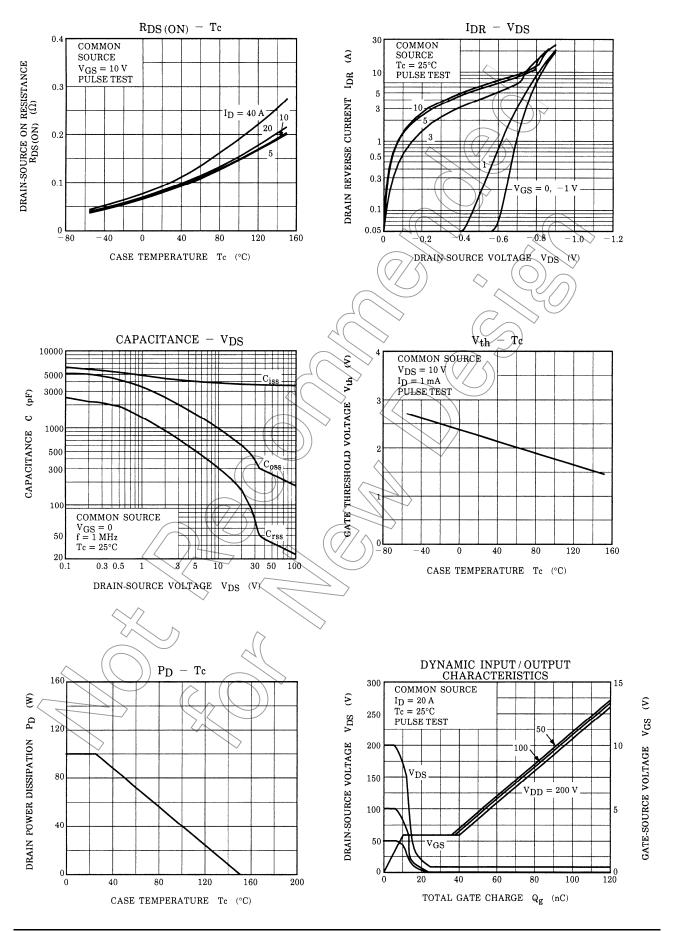
Not underlined: [[Pb]]/INCLUDES > MCV

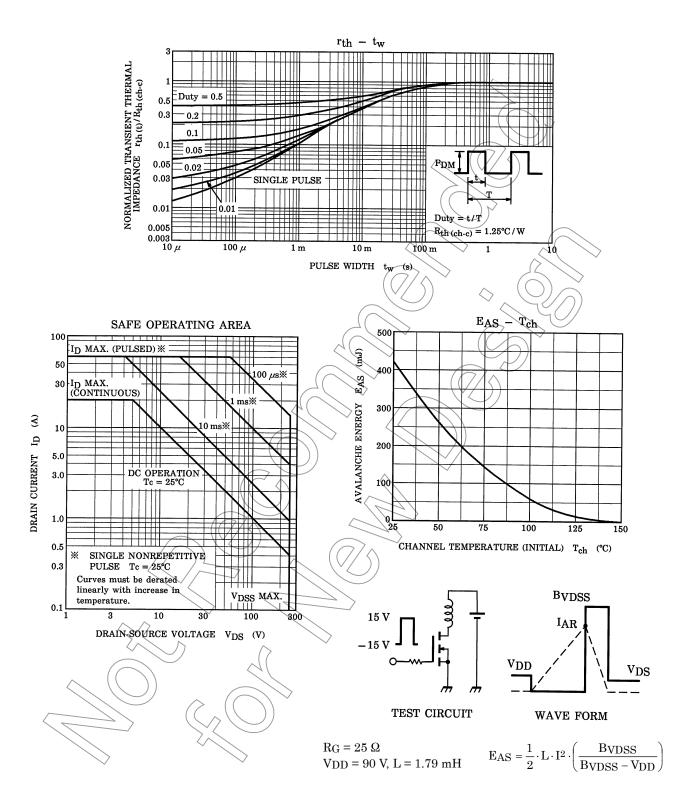
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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