COUNT	DESCRIPTION	IONS BY CHKD DATE			DATE	COUNT		DESCRIPTION OF REVISIONS		BY CHKD		DATE		
			-				Δ							
						į	Δ				l			
APPLICA	BLE STAND	ARD												
	VOLTAGE		250 V AC TEMPI						ERATURE RANGE -30 °C TO +85 °C(NOTE1)					
RATING		CURRENT		ЗА темре						ERATURE RANGE -10 °C TO +60 °C(NO				
APPLICABLE CABLE		UL1061,UL1007:AWG24								·2DSA	(* *	<)		
					SF	ECIFIC	ATI	ON	S					
IT	TEST METHOD							REQUIREMENTS					ΑT	
CONSTR														
GENERAL E								ACCORDING TO DRAWING.					X	
MARKING		CONFIRMED VISUALLY.											X	
		TERISTICS												
CONTACT R	,							30 mΩ MAX.				×	_	
INSULATION RESISTANC	500V DC							1000 MΩ MIN				×	_	
VOLTAGE P	650V AC FOR 1 min							NO RLASHOVER OR BREAKDOWN					_	
MECHAN	ICAL CHAP	RACTE	RIST	ics									اـــــا	
MECHANICA OPERATION	30TIMES INSERTIONS AND EXTRACTIONS.							 CONTACT RESISTANCE: 30 mΩ MAX. NO DAMAGE, CRACK AND LOSENESS OF PARTS. 				s ×	_	
VIBRATION	FREQUENCY 10 TO 55 Hz, SINGLE AMPLITUDE 0.75 mm, AT 2 h, FOR 3 DIRECTIONS.							 NO ELECTRICAL DISCONTINUITY OF 1 μs. NO DAMAGE, CRACK AND LOSENESS 				×	_	
SHOCK	490 m/s ² DURATION OF PULSE 11 ms AT 3 TIMES FOR 3 DIRECTIONS.							OF PARTS. ① NO ELECTRICAL DISCONTINUITY OF 1 μs.				X		
									② NO DAMAGE, CRACK AND LOSENESS OF PARTS.				<u> </u>	
DAMP HEAT	ENVIRONMENTAL CHARACTERISTICS DAMP HEAT EXPOSED AT 40 ± 2 °C. 90 \sim 95 %, 96 h. ① CONTACT RESISTANCE: 30 m Ω MAX.												Τ	
(STEADY STATE)		EXPOSED AT 40±2 °C, 90 ∼ 95 %, 96 h.							② INSULATION RESISTANCE:1000MΩ MIN. NO DAMAGE, CRACK AND LOSENESS					_
	OF PARTS.													
RAPID CHAI TEMPERATI	TEMPERATURE $-55 \rightarrow 5$ TO 35 $\rightarrow +85 \rightarrow 5$ TO 35 °C TIME 30 $\rightarrow 10$ TO 15 $\rightarrow 30 \rightarrow 10$ TO15min UNDER 5 CYCLES.							② INSULATION RESISTANCE:1000MΩ ③ NO DAMAGE, CRACK AND LOSENESS						
RESISTANC	SOLDER TEMPERATURE, 260±5°C FOR							OF PARTS. NO DEFORMATION OF CASE OF						
SOLDERING	IMMERSION, DURATION, 5S.							EXCESSIVE LOOSENESS OF THE					-	
OOL DEBAR	SOLDERED AT SOLDER TEMPERATURE, 230±5°C FOR IMMERSION DURATION, 3S.							TERMINALS. A NEW UNIFORM COATING OF SOLDER SHALL COVER A MINIMUM OF 95% OF THE SURFACE BEING IMMERSED.						
SOLDERABI														
REMARKS	DR						AWN						SED	
NOTE2:APP FOR AFTE HUM	LY TO THE CO UNUSED PRO ER PCB BOARI IDITY RANGE	EMPERATURE RISE BY CURRENT. INDITION OF LONG TERM STORAGE DUCTS BEFORE PCB ON BOARD, D,OPERATING TEMPERATURE AND IS APPLIED FOR INTERIM					M.Nal	kamote	22 64.03.22 64.03.22 04.03.22					
							13.2	2 64.03.22 04.03.22 04.03.22						
	erwise spec						<u> </u>							
Note QT:Q	ualification Test	AT:Ass	urance	Test		plicable Tes		•	PART	NO.				
HS	HIROSE ELECTRIC CO., LTD. SPECIFICATION SHEET DF3-*S-2DSA (25)													
CODE NO. (OLD) CL			ELC4-162329-10					CL543-						

