### Microwave Pulse Power Silicon NPN Transistor 350W (peak), 1025–1150MHz

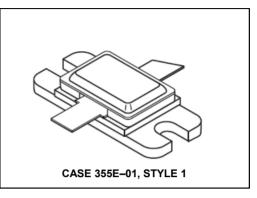
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Designed for 1025–1150 MHz pulse common base amplifier applications such as TCAS, TACAN and Mode–S transmitters.

- Guaranteed performance @ 1090 MHz Output power = 350 W Peak Gain = 8.5 dB min, 9.0 dB (typ.)
- 100% tested for load mismatch at all phase angles with 10:1 VSWR
- Hermetically sealed package
- Silicon nitride passivated
- Gold metallized, emitter ballasted for long life and resistance to metal migration
- Internal input and output matching
- Characterized using Mode-S pulse format

#### Product Image



#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V <sub>CES</sub>	65	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	65	Vdc
Emitter–Base Voltage	V <sub>EBO</sub>	3.5	Vdc
Collector Current — Peak (1)	lc	31	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C (1), (2) Derate above 25°C	PD	1590 9.1	Watts W/°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +200	°C
Junction Temperature	TJ	200	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case (3)		0.11	°C/W

NOTES:

- 1. Under pulse RF operating conditions.
- These devices are designed for RF operation. The total device dissipation rating applies only when the devices are operated as pulsed RF amplifiers.
- Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques. (Worst Case θ<sub>JC</sub> measured using Mode–S pulse train, 128 µs burst 0.5 µs on, 0.5 µs off repeating at 6.4 ms interval.)

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#### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)

Symbol	Min	Тур	Max	Unit
V(BR)CES	65	-	—	Vdc
V <sub>(BR)CBO</sub>	65	-	_	Vdc
V <sub>(BR)EBO</sub>	3.5	-	_	Vdc
I <sub>CBO</sub>	_	-	25	mAdc
h <sub>FE</sub>	20	_	_	_
G <sub>PB</sub>	8.5	9.0	_	dB
η	40	-	—	%
Ψ	N	o Degradation	in Output Pow	/er
	V(BR)CES V(BR)CBO V(BR)EBO ICBO hFE GPB	V(BR)CES  65    V(BR)CBO  65    V(BR)EBO  3.5    ICBO     hFE  20    GpB  8.5    η  40	V(BR)CES  65     V(BR)CBO  65     V(BR)EBO  3.5     ICBO      hFE  20     GPB  8.5  9.0    η  40	V(BR)CES  65      V(BR)CBO  65      V(BR)EBO  3.5      ICBO   25    hFE  20      GPB  8.5  9.0     η  40

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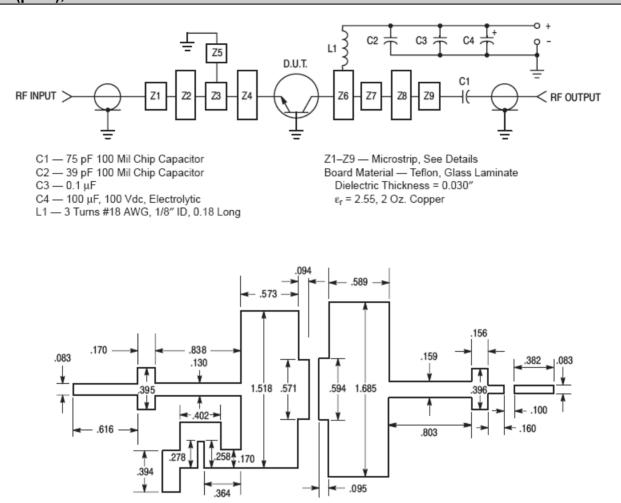


Figure 1. Test Circuit

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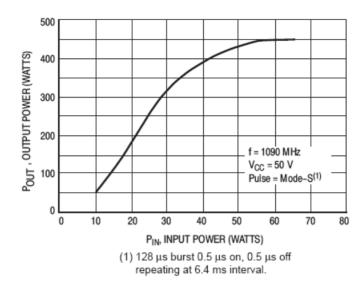


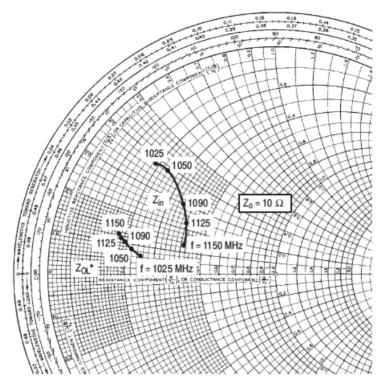
Figure 2. Output Power versus Input Power

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Pout = 350 W Pk Vo	c = 50 V
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f MHz	Z <sub>in</sub> OHMS	Z <sub>OL</sub> * (1) OHMS	
1025	1.92 + j3.80	2.52 + j0.70	
1050	2.44 + j3.92	2.18 + j0.85	
1090	3.55 + j3.02	1.94 + j1.13	
1125	4.11 + j2.27	1.80 + j1.22	
1150	4.13 + j1.35	1.71 + j1.31	

 $Z_{OL}^{\star}$  is the conjugate of the optimum load impedance into which the device operates at a given output power voltage and frequency.

Figure 3. Series Equivalent Input/Output Impedances

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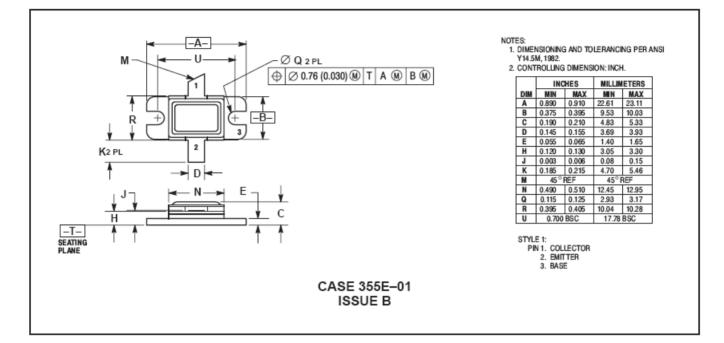
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#### PACKAGE DIMENSIONS





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