# DSU-FR EMULATOR LQFP-120P HEADER TYPE 3 MB2198-112-E OPERATION MANUAL



#### **PREFACE**

Thank you for purchasing the LQFP-120P header type 3 (MB2198-112-E) for the DSU-FR\*1 emulator.

This optional product for the DSU-FR emulator BGA-420P adapter (MB2198-110-E)\*2 is used to connect the DSU-FR emulator (MB2198-01-E) to a user system that uses MB91352, MB91353 or other FR series MCUs of the LQFP-120 package (package code: FPT-120P-M21). Consult the Sales Department for applicable MCUs and MCUs for evaluating applicability.

This manual explains how to operate the DSU-FR emulator LQFP-120P header type 3 (MB2198-112-E). Be sure to read this manual before using the product.

- \*1: FR, the abbreviation of FUJITSU RISC controller, is a line of products of FUJITSU MICRO-ELECTRONICS LIMITED.
- \*2: Referred to as the "adapter"

#### **■** European RoHS compliance

Products with a -E suffix on the part number are European RoHS compliant products.

#### ■ Notice on this document

All information included in this document is current as of the date it is issued. Such information is subject to change without any prior notice.

Please confirm the latest relevant information with the sales representatives.

#### ■ Caution of the product described in this document

The following precautions apply to the product described in this manual.



Indicates a potentially hazardous situation which could result in death or serious injury and/or a fault in the user's system if the product is not used correctly.

Cuts	This product has parts with sharp points that are exposed. Do not touch edge of the product with your bare hands.	
Damage	When connect the header board to the user system, correctly position the index mark (diagonally chipped corner) on the NQPACK mounted on the user system with the index mark (▼) on the header board, otherwise the emulator system and user system might be damaged	
Damage	When mounting a mass production MCU, correctly position pin 1, otherwise the mass production MCU and user system might be damaged.	

- The contents of this document are subject to change without notice.

  Customers are advised to consult with sales representatives before ordering.
- The information, such as descriptions of function and application circuit examples, in this document are presented solely for the purpose of reference to show examples of operations and uses of FUJITSU MICROELECTRONICS semi-conductor device; FUJITSU MICROELECTRONICS does not warrant proper operation of the device with respect to use based on such information. When you develop equipment incorporating the device based on such information, you must assume any responsibility arising out of such use of the information. FUJITSU MICROELECTRONICS assumes no liability for any damages whatsoever arising out of the use of the information.
- Any information in this document, including descriptions of function and schematic diagrams, shall not be construed as license of the use or exercise of any intellectual property right, such as patent right or copyright, or any other right of FUJITSU MICROELECTRONICS or any third party or does FUJITSU MICROELECTRONICS warrant non-infringement of any third-party's intellectual property right or other right by using such information. FUJITSU MICROELECTRONICS assumes no liability for any infringement of the intellectual property rights or other rights of third parties which would result from the use of information contained herein.
- The products described in this document are designed, developed and manufactured as contemplated for general use, including without limitation, ordinary industrial use, general office use, personal use, and household use, but are not designed, developed and manufactured as contemplated (1) for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could have a serious effect to the public, and could lead directly to death, personal injury, severe physical damage or other loss (i.e., nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system), or (2) for use requiring extremely high reliability (i.e., submersible repeater and artificial satellite).
  - Please note that FUJITSU MICROELECTRONICS will not be liable against you and/or any third party for any claims or damages arising in connection with above-mentioned uses of the products.
- Any semiconductor devices have an inherent chance of failure. You must protect against injury, damage or loss from such failures by incorporating safety design measures into your facility and equipment such as redundancy, fire protection, and prevention of over-current levels and other abnormal operating conditions.
- Exportation/release of any products described in this document may require necessary procedures in accordance with the regulations of the Foreign Exchange and Foreign Trade Control Law of Japan and/or US export control laws.
- The company names and brand names herein are the trademarks or registered trademarks of their respective owners.

# 1. Checking the Delivered Product and Accessories

The composition of this product is shown in the following. Before using this product, confirm that all the parts listed below were received.

Header board
 NQPACK120SD (manufactured by Tokyo Eletech Corporation)
 HQPACK120SD (manufactured by Tokyo Eletech Corporation)
 Header board mounting screws
 Operation manual (Japanese version)
 Operation manual (English version, this manual)

# 2. Handling Precautions

The environmental specification of this product is shown in Table 1.

Table 1 Environmental specification

Item	Temperature	Relative humidity
Operation	5 °C to 35 °C	20% to 80% (without condensation)
Storage	0 °C to 70 °C	20% to 80% (without condensation)

The LQFP-120P header type 3 (MB2198-112-E) for the DSU-FR emulator is carefully designed to provide reliable contact between the parts by using a sophisticated structure and precise dimensions. Because of this, the adapter unit is less sturdy than conventional products of this kind. Always use the adapter unit correctly in the prescribed environment. For details about connection of header board, see "3. Using the Product".

# 3. Using the Product

Before using this product, install the accessory NQPACK120SD in the user system as an IC socket for MCU, to connect the header board to a user system.

#### ■ Connecting the user system

Connect the header board to the user system as follows:

- 1. Mount the header of the header board on the NQPACK120SD in the correct orientation by aligning the index mark (▼) on the header board with the index mark (diagonally chipped corner) of the NQPACK120SD.
- 2. Secure the header board to the NQPACK120SD with the header board mounting screws.
- 3. Connect the header board to the adapter board with the header interface cable. For details about this connection, refer to the operation manual of the DSU-FR emulator BGA-420P adapter (MB2198-110-E).

Figure 1 is a diagram of the connection of the header board to the user system.

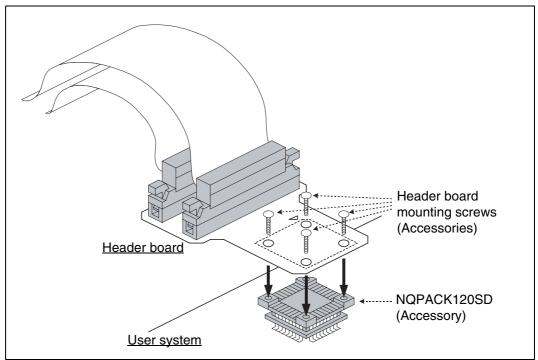


Figure 1 Header board connection to the user system

Note: The engagement between the board section and the header section of the header board is very fragile. Be sure to insert or extract the header from the board vertically. Be sure the header board connection is not forced.

#### ■ Connecting a mass production MCU

A mass production MCU can be connected to the NQPACK120SD using the accessory HQPACK120SD. Connect the NQPACK120SD as follows:

- 1. Mount the mass production MCU on the NQPACK120SD in the correct orientation by aligning the index mark (● indent) on the MCU with the index mark (diagonally chipped corner) of the NQPACK120SD.
- 2. Mount the accessory HQPACK120SD on the NQPACK120SD in the correct orientation by aligning the index mark (diagonally chipped corner).
- 3. Secure the HQPACK120SD on the NQPACK120SD with the HQPACK120SD mounting screws.

Figure 2 is a diagram of connecting the mass production MCU to the user system.

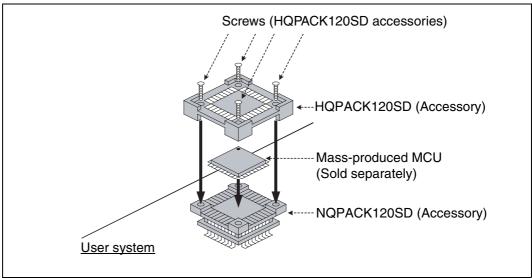


Figure 2 Mass production MCU connection to the user system

# 4. Restrictions

## ■ NQPACK120SD mounting restrictions

Once the header board is connected to the user system, the heights of parts mounted in the space around the NQPACK120SD are restricted.

The PC board of the user system must be designed with due consideration given to this restriction.

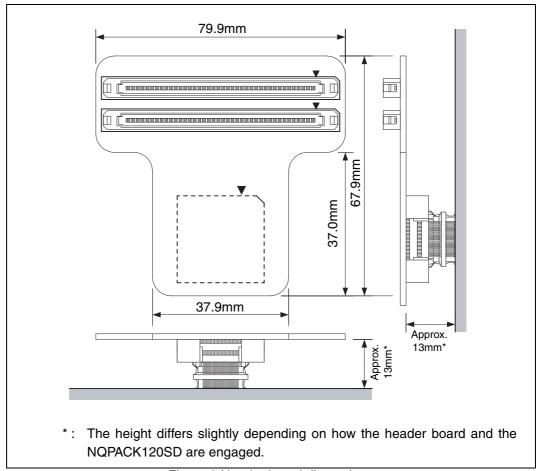


Figure 3 Header board dimensions

#### **■** MCU footprint design notes

Figure 4 shows the recommended dimensions of the NQPACK120SD footprint mounted on the PC board of the user system. The PC board of the user system must be designed with due consideration given to this footprint as well as to the mass production MCU.

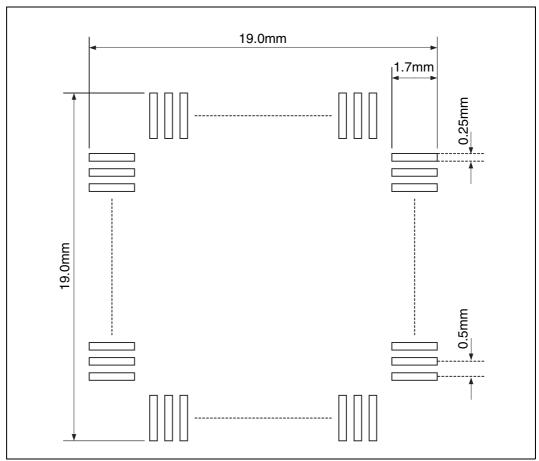


Figure 4 Recommended dimensions of the footprint for mounting the NQPACK120SD

SS01-71121-1E

## FUJITSU MICROELECTRONICS • SUPPORT SYSTEM

DSU-FR EMULATOR LQFP-120P HEADER TYPE 3 MB2198-112-E OPERATION MANUAL

July 2008 the first edition

Published FUJITSU MICROELECTRONICS LIMITED

Edited Business & Media Promotion Dept.

# **FUJITSU**