TOSHIBA Photocoupler GaAs IRed & Photo-Triac

# TLP525G, TLP525G-2, TLP525G-4

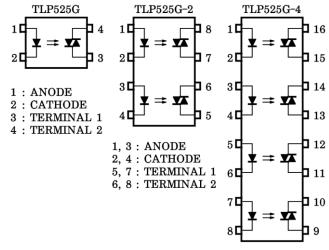
Triac Drive
Programmable Controllers
AC-Output Module
Solid State Relay

The TOSHIBA TLP525G, -2 and -4 consist of a photo–triac optically coupled to a gallium arsenide infrared emitting diode.

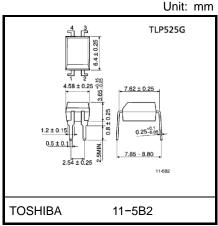
The TLP525G-2 offers two isolated channels in an eight lead plastic DIP package, while the TLP525G-4 provides four isolated channels in a sixteen lead plastic DIP package.

- Peak off-stage voltage: 400 V (min)
- Trigger LED current: 10 mA (max)
- Peak on-stage current: 2 Apk (max)
- Isolation voltage: 2500 V<sub>rms</sub> (min)
- UL approved: UL1577, File No.E67349
- cUL approved :CSA Component Acceptance Service No. 5A, File No.E67349

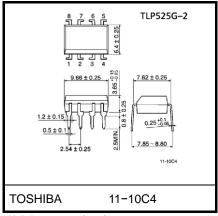
#### Pin Configurations (top view)



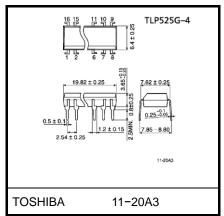
1, 3, 5, 7 : ANODE 2, 4, 6, 8 : CATHODE 9, 11, 13, 15 : TERMINAL 1 10, 12, 14, 16 : TERMINAL 2



Weight: 0.26g (typ.)



Weight: 0.54g (typ.)



Weight: 1.1g (typ.)



### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristic				Rat		
			Symbol	TLP525G	TLP525G-2 TLP525G-4	Unit
LED	Forward current		lF	50	50	mA
	Forward current derating		IF/°C	–0.7 (Ta ≥ 53°C)	-0.5 (Ta ≥ 25°C)	mA / °C
	Pulse forward current		lFP	1 (100µs pu	Α	
	Reverse voltage		VR	Ę	V	
	Input power dissipation		PD	50	60	mW
	Input power dissipation derating		ΔP <sub>D</sub> /°C	-0.69(Ta ≥ 53°C)	-0.6(Ta ≥ 25°C)	mW/°C
	Junction temperature		Tj	125		°C
	Off-state output terminal voltage		VDRM	400		V
	On-state RMS current	Ta = 25°C	I= ()	100	80	^
		Ta = 70°C	IT (RMS)	50	40	mA
١.	On-state current derating (Ta ≥ 25°C)		I <sub>T</sub> / °C	-1.1	-0.9	mA / °C
Detector	Peak on state current		lTP	2 (100μs pulse, 120pps)		А
Det	Peak non-repetitive surge current (P <sub>W</sub> = 10ms)		ITSM	1.2		А
	Output power dissipation		Ро	300	240	mW
	Output power dissipation derating (Ta ≥ 25°C)		ΔP <sub>o</sub> /°C	-3.0	-2.4	mW / °C
	Junction temperature		Tj	115		°C
Stor	Storage temperature range		T <sub>stg</sub>	−55 to 125		°C
Operating temperature range		Topr	-40 to 100		°C	
Lead soldering temperature		T <sub>sol</sub>	260 (10s)		°C	
Isola	Isolation voltage (Note)		BVs	2500 (AC, 1minute, R.H. ≤ 60%)		V <sub>rms</sub>

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note: Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.

#### **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	VAC	_	_	120	Vac
Forward current	lF	15	20	25	mA
Peak on-state current	ITP	_	_	1	Α
Operating temperature	T <sub>opr</sub>	-25	-	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

2



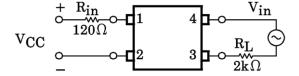
## Individual Electrical Characteristics (Ta = 25°C)

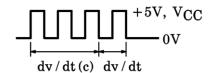
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	VF	IF = 10mA	1.0	1.15	1.3	V
	Reverse current	IR	V <sub>R</sub> = 5V	_	_	10	μА
	Capacitance	CT	VF = 0 V, f = 1MHz	_	30	_	pF
Detector	Peak off-state current	I <sub>DRM</sub>	V <sub>DRM</sub> = 400V	_	10	100	nA
	Peak on-state voltage	V <sub>TM</sub>	I <sub>TM</sub> = 100mA	_	1.7	3.0	V
	Holding current	lΗ	_	_	0.6	1	mA
	Critical rate of rise of off–state voltage	dv / dt	$V_{in} = 120V_{rms}$ , $Ta = 85^{\circ}C$ (Figure 1)	200	500	l	V / μs
	Critical rate of rise of commutating voltage	dv / dt (c)	$V_{in} = 30V_{rms}$ , $I_T = 15mA$ (Figure 1)	_	0.2		V / μs

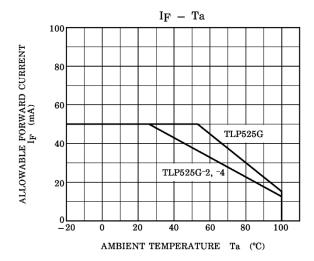
## **Coupled Electrical Characteristics (Ta = 25°C)**

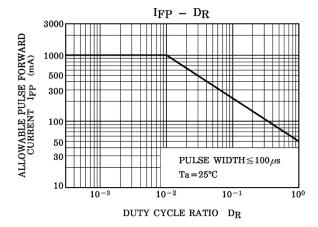
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	l <sub>FT</sub>	VT = 3V	_	5	10	mA
Capacitance input to output	Cs	V <sub>S</sub> = 0 V, f = 1MHz	_	0.8	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500V, R.H. ≤ 60%	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
	BVS	AC, 1 minute	2500	_	_	- Vrms
Isolation voltage		AC, 1 second, in oil	_	5000	_	
		DC, 1 minute, in oil	_	5000	_	Vdc

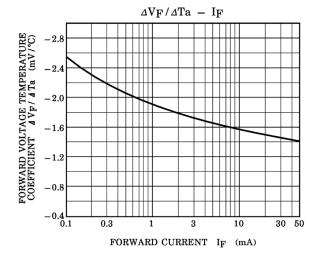
Fig.1 dv / dt Test Circuit

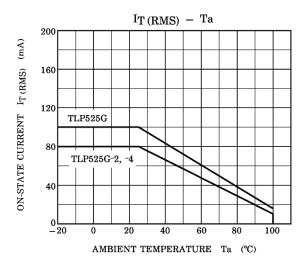


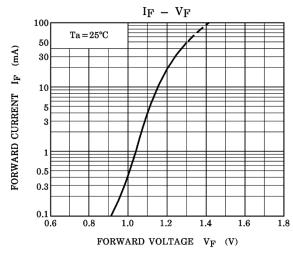


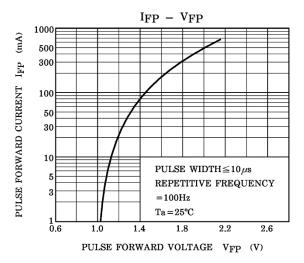


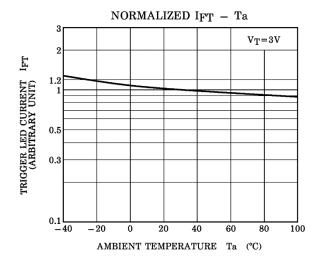


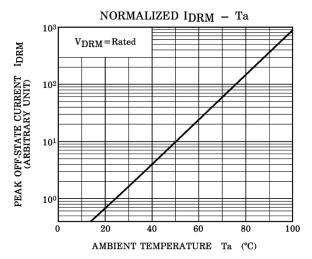


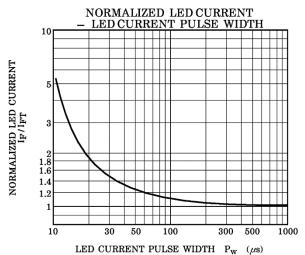


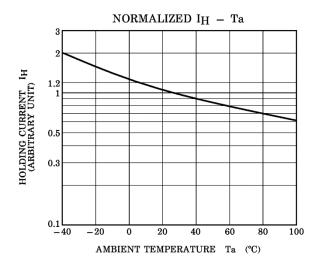


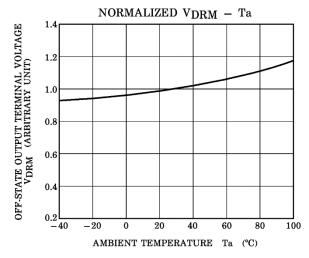












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