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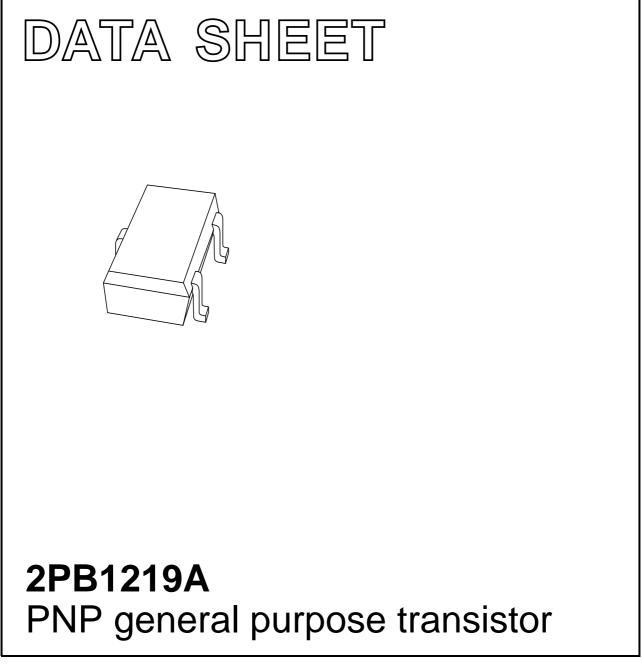
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Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1997 Mar 25 1999 Apr 12



PNP general purpose transistor

FEATURES

- High current (max. 500 mA)
- Low voltage (max. 50 V)
- Low collector-emitter saturation voltage (max. 600 mV).

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

PNP transistor in a SOT323; SC70 plastic package. NPN complement: 2PD1820A.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
2PB1219AQ	D*Q
2PB1219AR	D*R
2PB1219AS	D*S

Note

- 1. * = : Made in Hong Kong.
 - * = t : Made in Malaysia.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	-60	V
V _{CEO}	collector-emitter voltage	open base	-	-50	V
V _{EBO}	emitter-base voltage	open collector	-	-5	V
I _C	collector current (DC)		-	-500	mA
I _{CM}	peak collector current		-	-1	А
I _{BM}	peak base current		-	-200	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \text{ °C}$; note 1	-	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Refer to SOT323; SC70 standard mounting conditions.

Fig.1 Simplified outline (SOT323; SC70) and symbol.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	

Product data sheet

PNP general purpose transistor

2PB1219A

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	625	K/W

Note

1. Refer to SOT323; SC70 standard mounting conditions.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	$I_E = 0; V_{CB} = -20 V$	_	-100	nA
		I _E = 0; V _{CB} = -20 V; T _j = 150 °C	_	-5	μA
I _{EBO}	emitter cut-off current	$I_{C} = 0; V_{EB} = -4 V$	_	-100	nA
h _{FE}	DC current gain	$I_{C} = -150 \text{ mA}; V_{CE} = -10 \text{ V}; \text{ note } 1$			
	2PB1219AQ		85	170	
	2PB1219AR		120	240	
	2PB1219AS		170	340	
h _{FE}	DC current gain	$I_{C} = -500 \text{ mA}; V_{CE} = -10 \text{ V}; \text{ note } 1$	40	-	
V _{CEsat}	collector-emitter saturation voltage	$I_{C} = -300 \text{ mA}; I_{B} = -30 \text{ mA}; \text{ note } 1$	_	-600	mV
V _{BEsat}	base-emitter saturation voltage	IC = -300 mA; IB = -30 mA; note 1	_	-1.5	V
C _c	collector capacitance	$I_E = i_e = 0; V_{CB} = -10 V; f = 1 MHz$	_	15	pF
f _T	transition frequency	$I_{C} = 50 \text{ mA}; V_{CE} = -10 \text{ V};$			
	2PB1219AQ	f = 100 MHz; note 1	100	-	MHz
	2PB1219AR		120	-	MHz
	2PB1219AS		140	-	MHz

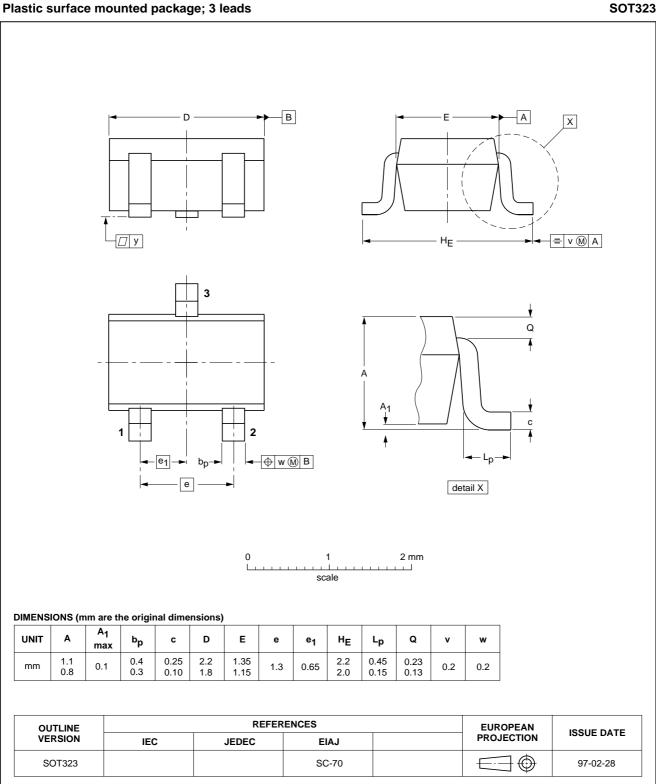
Note

1. Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

2PB1219A

PNP general purpose transistor

PACKAGE OUTLINE



PNP general purpose transistor

2PB1219A

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
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NXP Semiconductors

Customer notification

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Contact information

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